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Applicants note with appreciation the detailed response in the present office action. Claims 1, 4-11, 16, and 19 are amended in compliance with the Examiner's comments. The specification is amended to correct typographical errors, to correspond with the claim amendments, and to provide support in the detailed description for subject matter recited in Claims 1 and 9. No new matter is introduced by these amendments. Claims 1-20 are pending. Reconsideration is respectfully requested based on the amendments and remarks herein.

Claims 1-20 were rejected under 35 U.S.C. 112, second paragraph. Applicants have amended independent Claims 1 and 16 to particularly point out and distinctly claim the subject matter that Applicants regard as their invention. In particular, Claim 1 is amended to recite "a first gate member formed on a first layer" and "a second gate member formed on the first layer" and is supported by the application as filed, where the gate members are formed on the gate insulating layer 140 (Specification page 7 lines 1-16 and Fig. 2b). Claim 1 is also amended to recite "a first data member formed on a second layer" and "a first connection formed on the second layer" and is supported by the application as filed, where the data members are formed on the interlayer insulating layer 160 (Specification page 8 lines 22-30 and Fig. 2b). Dependent

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Claims 5-11 are amended to recite the corresponding first layer and/or the second layer, respectively.

Further, Claim 16 is amended to recite "forming a plurality of connections connected to the first and the second conductive members through the first contact holes" and "forming a plurality of data members connected to the N type and the P type impurity regions through the second contact holes" and is supported by the application as filed (Specification page 8 lines 22-30 and Fig. 2b).

The Examiner raised a question about the "data member" of Claim 16 in reference to the "first and second voltage supplying lines" of Claim 19. Claim 19 is amended to remove the word "supplying" in accordance with the disclosure which omits that word in describing "first voltage line 170a" and "second voltage line 170b" (Specification page 8 line 31 to page 9 line 6). Applicants respectfully traverse the assertion that data members 170a and 170b are power supply lines, and instead Applicants submit that these data members merely conduct signal voltage levels corresponding to low (Vss) and high (Vdd) supply voltages in accordance with standard metal oxide semiconductor (MOS) terminology. The disclosure further teaches the low voltage level corresponds to a gate-off voltage for turning off the thin film transistor, while the high voltage level corresponds to a gate-on voltage for turning on the thin film transistor

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(Specification page 8 lines 31-32 and page 9 lines 3-4). In this manner, lines 170a and 170b should properly be considered data members since they conduct data signals that operate the TFTs. In view of the amendments and remarks herein, Applicants respectfully request the rejection of Claims 1-20 under 35 U.S.C. 112 be withdrawn.

The specification is amended to correct typographical errors, to correspond with the claim amendments, and to provide support in the detailed description for "first and second semiconductor members formed on the substrate and having opposite conductivity" recited in Claims 1 and for "third and fourth semiconductor members formed on the substrate and having opposite conductivity" recited in Claim 9 as is supported elsewhere in the disclosure (Specification page 2 line 19 and page 3 lines 23-25).

If the Examiner has any question about any aspect of the present application, a telephone call to the undersigned at (949) 752-7040 is both welcomed and encouraged.

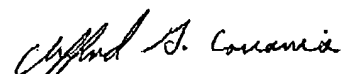
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